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Docket No.: H0498.70173US00

1651
JW

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Rahul Singhvi et al.
Serial No.: 10/032280
Confirmation No.: 6387
Filed: December 21, 2001
For: METHOD OF FORMATION OF MICROSTAMPED PATTERNS ON PLATES FOR ADHESION OF CELLS AND OTHER BIOLOGICAL MATERIALS, DEVICES
Examiner: D. K. Ware
Art Unit: 1651

Certificate of Mailing Under 37 CFR 1.8(a)

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the U.S. Postal Service on the date shown below with sufficient postage as First Class Mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Dated: 1-6-06TRANSMITTAL LETTER

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Enclosed are the following items for filing in connection with the above-referenced Patent Application:

1. Request for Corrected Filing Receipt;
2. Copy of Filing Receipt with changes marked in red;
3. Copy of first page of specification; and
4. Return Receipt Postcard.

The Director is hereby authorized to charge any deficiency in the fees filed, asserted to be filed or which should have been filed herewith (or with any paper hereafter filed in this application by this firm) to our Deposit Account No. 23/2825, under Docket No. H0498.70173US00. A duplicate copy of this paper is enclosed.

Dated: January 6, 2006

Respectfully submitted,

By

Timothy J. Oyer, Ph.D.

Registration No.: 36,628

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(PATENT)

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Dated: 1-6-06

1-6-06
TINA HANIFIN

REQUEST FOR CORRECTED FILING RECEIPT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

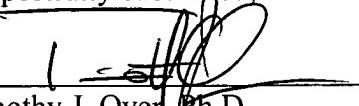
Dear Sir:

Applicant hereby requests that a corrected Filing Receipt be issued in the above-identified patent application. The official Filing Receipt received by Applicant, a copy of which is attached hereto, has an error in the "Domestic Priority data as claimed by Applicant". The date for U.S. Patent Application Serial No. 08/659,537 is incorrectly listed as 06/06/1996. The correct date, as submitted under "Related Applications" on the application as filed December 21, 2001, is 06/07/1996. A copy of the first page of the specification is enclosed.

Applicant additionally requests that all pertinent U.S. Patent and Trademark Office records relating to the subject application be changed to reflect this correction.

Dated: January 6, 2006

Respectfully submitted,

By 

Timothy J. Oyer, Ph.D.

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UNITED STATES PATENT AND TRADEMARK OFFICE

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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY.DOCKET.NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
10/032,280	12/21/2001	1651	870	H00498/70173 TJO	1	1	1

23628
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✓ CONFIRMATION NO. 6387 ✓
UPDATED FILING RECEIPT



OC000000007836445

Date Mailed: 04/10/2002

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

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Domestic Priority data as claimed by applicant

THIS APPLICATION IS A CON OF 09/373,334 08/12/1999 PAT 6,368,838
WHICH IS A CON OF 08/951,886 10/16/1997 PAT 5,976,826
WHICH IS A CON OF 08/659,537 ~~06/06/1996~~ PAT 5,776,748 *
WHICH IS A CON OF 08/131,838 10/04/1993 ABN ~~06/07/1996~~
(*) Data inconsistent with PTO records.

Foreign Applications

If Required, Foreign Filing License Granted 02/22/2002

Projected Publication Date: 07/18/2002 ✓

Non-Publication Request: No

Early Publication Request: No

Title

Method of formation of microstamped patterns of plates for adhesion of cells and other biological materials, devices and uses therefor

Preliminary Class

435

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Title 35, United States Code, Section 184
Title 37, Code of Federal Regulations, 5.11 & 5.15**

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METHOD OF FORMATION OF MICROSTAMPED PATTERNS OF PLATES FOR ADHESION OF CELLS AND OTHER BIOLOGICAL MATERIALS, DEVICES AND USES THEREFOR

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Related Applications

This application is a continuation of copending U.S. Patent Application Serial No. 09/373,334, filed August 12, 1999, which is a continuation of U.S. Patent Application Serial No. 08/951,886, filed October 16, 1997, which is a continuation of U.S. Patent Application Serial No. 08/659,537, filed June 7, 1996, now U.S. Patent No. 5,776,748, which is a continuation of 10 U.S. Patent Application Serial No. 08/131,838 filed October 4, 1993.

Field of the Invention

The present invention relates generally to derivatization and patterning of surfaces, more particularly to the formation on surfaces of patterns of self-assembled molecular monolayers with 15 differing bioadhesive properties using a microstamp, novel articles produced thereby, and uses therefor.

This invention was made with government support NSF Grant Number EEC-880-3014, NIH Grant Number GM30367, ONR Grant Number N00014-86-K-0756, and ACS Grant Number CD-493. The government has certain rights to the invention.

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Background of the Invention

In the field of microelectronic devices and sensors, the development of devices that are small relative to the state of the art, conveniently and relatively inexpensively reproduced, and produced with a relatively low failure rate has long been important. In the fields of cellular and 25 developmental, and molecular biology, microbiology, biomedical devices, and biotechnology, there is now a growing need for devices of similar scale with features as small as or smaller than individual cells.

In the electronics industries, such devices have been produced by a variety of methods. A well-known method of production of such devices is photolithography. According to this 30 technique, a thin film of conducting, insulating, or semiconducting material is deposited on a substrate and a negative or positive resist (photoresist) is coated onto the exposed surface of the